



Image

2814

501.40724X00

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): M. Sahara, et al.

Application No.: 09/974,814

Filing Date: October 12, 2001

For: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE AND THE
PROCESS OF THE SAME

Art Unit: 2814

Examiner: T. Quach

AMENDMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

February 13, 2004

Sir:

In response to the Office Action mailed January 13, 2004, please amend the
above-identified application as listed in the following, and as set forth on the following
pages:

Amendments to the Claims; and

Remarks are included following the amendments.